Docket No.: 57454-180



In re Application of

Toyohiko YOSHIDA

Serial No.:

Group Art Unit:

Filed: July 25, 2001

Examiner:

For:

DATA PROCESSING DEVICE WITH INSTRUCTION TRANSLATOR AND

MEMORY INTERFACE DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 57454-180		SERIAL NO.		139	
				APPLICANT Toyohiko YOSHIDA			911 /911		
(PTO-1449)				FILING DATE July 25, 2001		GROUP	310 00 00 00 00		
	<u></u>	U	S. PATENT	DOCUMENTS	S				
EXAMINER'S INITIALS	PATENT NO.	DATE		NAME	CLASS	SUBCLASS	FIL	ING DATE	
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		FOR	EIGN PATI	ENT DOCUME	NTS				
EXAMINER'S INITIALS	PATENT NO.	PATENT NO. DATE		CLA CLA		S SUBCLASS	Translation		
	EP0709767	5/1/1996	Europe (cou Japanese Pa Open No. 9	tent Laying-			Yes	No .	
	9-26876	1/28/1997	Japan						
		<u> </u>					-		
	OTHER	 R ART (Inch	l uding Autho	r. Title, Date. Pe	ertinent P	ages, Etc.)	<u> </u>		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Hank SHIFFMAN, "JSTAR: Practical Java Acceleration For Information Appliances", JEDI Technologies, April 17, 2000.									
Tom R. HALFHILL, "JSTAR Coprocessor Accelerates Java, Licensable Bytecode Translator Works With Almost Any Core", Microprocessor Report, March 27, 2000.									
	James L. TURLEY, "Thumb Squeezes ARM Code Size, New Core Module Provides Optimized Second Instruction Set", MICROPROCESSOR REPORT, Vol. 9, No. 4, March 27, 1995.								
EXAMINER DATE CONSIDERED									